

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: ISHIHARA et al

Application No.: Continuation of U.S. Patent Application  
Serial No. 09/949,091, filed September 10, 2001

Filed: July 1, 2003

For: IC CARD APPARATUS HAVING PATTERNED INSULATING  
LAYER FORMED BETWEEN GATE INSULATING FILM AND  
SEMICONDUCTOR LAYER EXCEPT FOR CHANNEL REGION  
AND METHOD OF MANUFACTURE (AS AMENDED)

Prior Art Unit: 2871

Prior Examiner: Tarifur Rashid CHOWDHURY

**CLAIM FOR PRIORITY**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

July 1, 2003

Sir:

Pursuant to 35 USC §119 and 37 CFR §1.55, Applicants hereby claim the right  
of priority based on Japanese Patent Application No. 11-069529, filed in Japan on  
March 16, 1999.

A certified copy of the above-identified Japanese Patent Application was  
submitted on March 16, 2000, in prior Application No. 09/526,557, filed March 16, 2000.

Respectfully submitted,

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Attachment  
WIS/nbf